ABSTRACT

A driver circuit is provided for a stepper motor which utilizes a processor that outputs a PWM signal. The driver circuit includes an H-bridge circuit having first and second inputs, and a switching circuit having an input and an output. The first H-bridge input and the switching input connect to the PWM signal output from the processor, and the switching output connects to the second H-bridge input. The switching circuit performs an inversion of a duty cycle of the PWM signal when the PWM signal is present, and a non-inversion of the duty cycle when the PWM signal is not present.

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